EGC220 Class Notes 12/6/2021

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Final: Thursday 12/16, 10:15 AM - 12:15 PM

Closed book and notes, no calculator

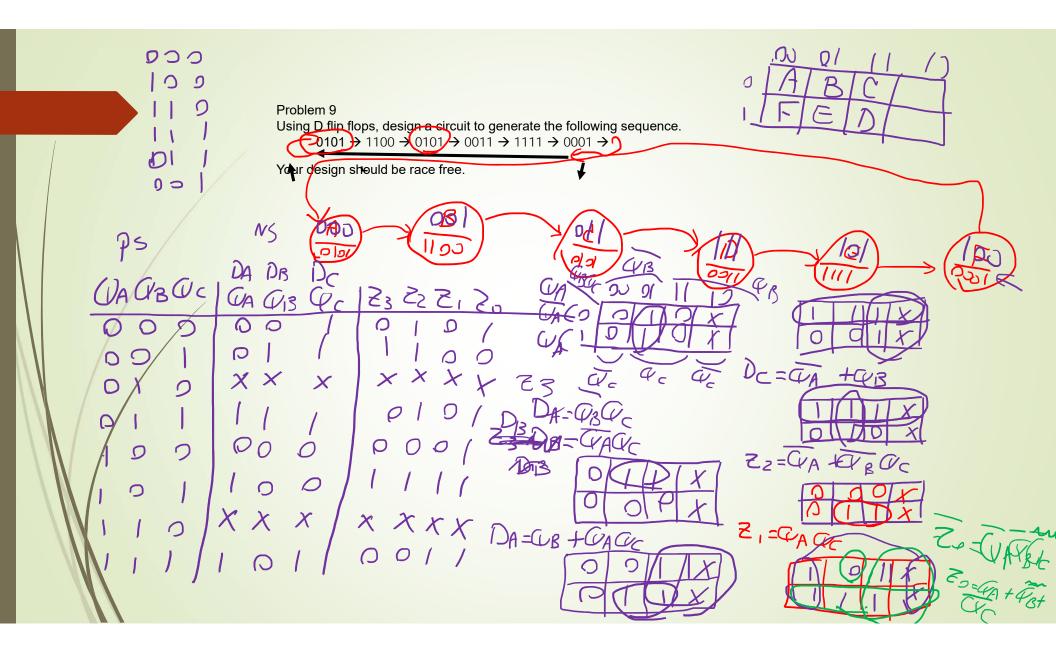
Number systems

- Simplification using K-map
 - SOP, POS, Standard SOP and POS, Min. SOP and POS

Design of combinational circuits

- Circuit conversion to all NAND or NOR gates
- Multiplexers, Demultiplexers, Decoders, Encoders
- Design of combinational circuits using PLD's

- Latch and flip flops characteristics and excitation tables, design of ripple counters
- Analysis of sequential circuits
- Design of sequential circuits
 - Design using Mealy and Moore model
 - Design of a sequence detector
 - Design of a shift register
 - Design of a controller



$$84 - 96 = -12 \qquad 84 \quad 3246 \quad 8421 \\ 96 \quad 01 \quad 01 \quad 0100 \\ -96 \quad 10 \quad 100000 \\ + 10100000 \\ 01010100 \quad 2'3 \quad -0000 \quad 8421 \\ -12 \\ -1$$

subtraction 1 unsigned 001.0 9.5 $\leftarrow 00 00$ + 1011.11 -0|00.0|0/01.01 $(23,5)_{c}$ 15:6=2 R=31 <y=1 (34.3)(34.3) $7 \div 6 = 1 R = 1$ 1153 20-6-3R=2 4 32 5 3 15+6=2 R=3 10+6=1 R=4 8:6=1 R=2 352.13)